

Form PTO-1449

INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION

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Atty Docket No.

P-6467

Serial No.

09/927,566

Applicant(s)

Swee Yew Choe et al.

Filing Date

August 9, 2001

Group

2819

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DC	AA	4,247,791	01/27/81	Rovell	307	238	04/03/78
DC	AB	5,859,548	01/12/99	Kong	326	113	07/31/96
	AC						
	AD						
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	AF						
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	AH						
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	AJ						
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## FOREIGN PATENT DOCUMENTS

							Translation	
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
	AL							
	AM							
	AN							
	AO							
	AP							

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

DC	AR	Choe et al., "Dynamic Half Rail Differential Logic for Low Power", IEEE 1997, pages 1936 to 1939
DC	AS	Jung et al., "Modular Charge Recycling Pass Transistor Logic (MCRPL)", Electronics Letters, 2nd March 2000 Vol. 36 No.5, March 2, 2000, Pages 404 to 405
DC	AT	Kong et al., "Charge Recycling Differential Logic for Low-Power Application", ISSC96 secession 18, IEEE 0-780331962/98, 1998, Pages 302 to 448

Examiner

Daniel Chay

Date Considered

7/23/02

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

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DC	AR	Choe et al., "Half Rail Differential Logic", ISSCC97/Secession 25/Processors and Logic/Paper SP 25.6 IEEE 0-7803-3721-2/97, 1997, pages 420 to 421, 336 to 337 and 489
DC	AS	Won et al., "Modified Half Rail Differential Logic for Reduced Internal Logic Swing", IEEE 0-7803-4455-3/98, 1998, pages II-157 to II-160
DC	AT	Kong et al., "Charge Recycling Differential Logic (CRDL) for Low-Power Application", IEEE Journal of Solid-State Circuits, Vol. 31, No. 9, September 1996, pages 1267 to 1276

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